

Abstract of the Disclosure

An apparatus for driving a plasma display panel includes a video processor, a logic controller, an address driver, an X-driver, and a Y-driver. XY-electrode line pairs of the plasma display panel are divided into a plurality of XY-electrode line pair groups. At least one of the  
5 X-driver and the Y-driver includes a plurality of driving circuits corresponding to the plurality of XY-electrode line pair groups, respectively. The plurality of driving circuits separately operate so that an addressing and a display-sustain discharge are alternately performed and an alternating current voltage provoking a display-sustain discharge is applied only to XY-electrode line pair groups of which an addressing has been completed.